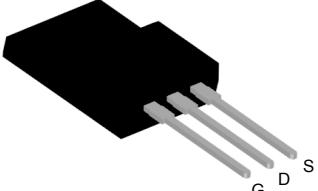
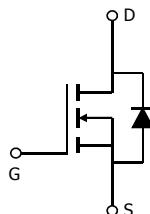


TMTF2N60 N-CHANNEL POWER MOSFET

General Description	Product Summary								
<p>The TMTF2N60 have been fabricated using an advanced high voltage MOSFET process that is designed to deliver high levels of performance and robustness in popular AC-DC applications. By providing low $R_{DS(on)}$, C_{iss} and C_{rss} along with guaranteed avalanche capability these parts can be adopted quickly into new and existing offline power supply designs.</p>	<table style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 30%; vertical-align: top;"> V_{DS}</td><td style="width: 30%; text-align: right;">600V</td></tr> <tr> <td>I_D (at $V_{GS}=10V$)</td><td style="text-align: right;">2A</td></tr> <tr> <td>$R_{DS(ON)}$ (at $V_{GS}=10V$)</td><td style="text-align: right;">$< 4.4\Omega$</td></tr> <tr> <td colspan="2" style="text-align: center; padding-top: 10px;"> 100% UIS Tested! 100% R_g Tested! </td></tr> </table> <div style="text-align: right; margin-top: 10px;">  </div>	V_{DS}	600V	I_D (at $V_{GS}=10V$)	2A	$R_{DS(ON)}$ (at $V_{GS}=10V$)	$< 4.4\Omega$	100% UIS Tested! 100% R_g Tested!	
V_{DS}	600V								
I_D (at $V_{GS}=10V$)	2A								
$R_{DS(ON)}$ (at $V_{GS}=10V$)	$< 4.4\Omega$								
100% UIS Tested! 100% R_g Tested!									

Top View TO-220F	
	

Absolute Maximum Ratings $T_A=25^\circ C$ unless otherwise noted					
Parameter	Symbol	Maximum	Units		
Drain-Source Voltage	V_{DS}	600	V		
Gate-Source Voltage	V_{GS}	± 30	V		
Continuous Drain Current ^B	I_D	2	A		
$T_C=100^\circ C$		1.4			
Pulsed Drain Current ^C	I_{DM}	8			
Avalanche Current ^C	I_{AR}	2			
Repetitive avalanche energy ^C	E_{AR}	60	mJ		
Single pulsed avalanche energy ^H	E_{AS}	120	mJ		
Peak diode recovery dv/dt	dv/dt	5	V/ns		
$T_C=25^\circ C$	P_D	56.8	W		
Power Dissipation ^B Derate above $25^\circ C$		0.45			
Junction and Storage Temperature Range	T_J, T_{STG}	-50 to 150	$^\circ C$		
Maximum lead temperature for soldering purpose, 1/8" from case for 5 seconds	T_L	300	$^\circ C$		

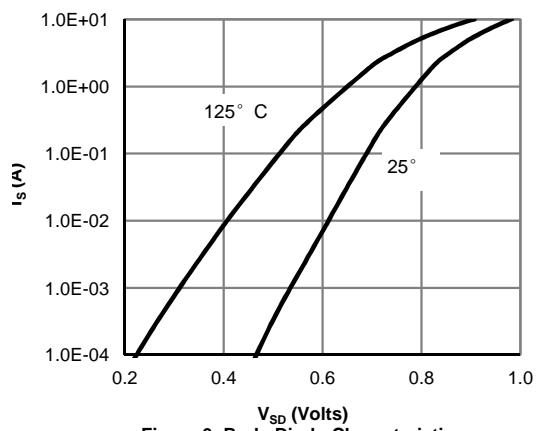
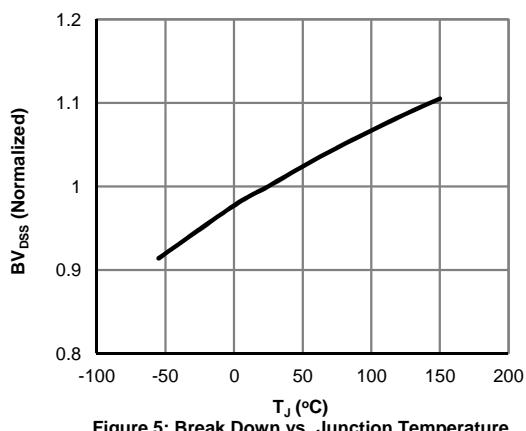
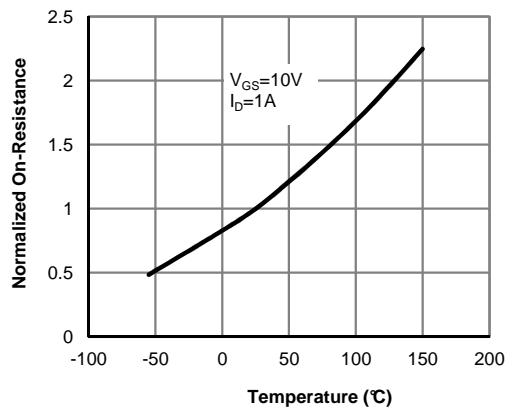
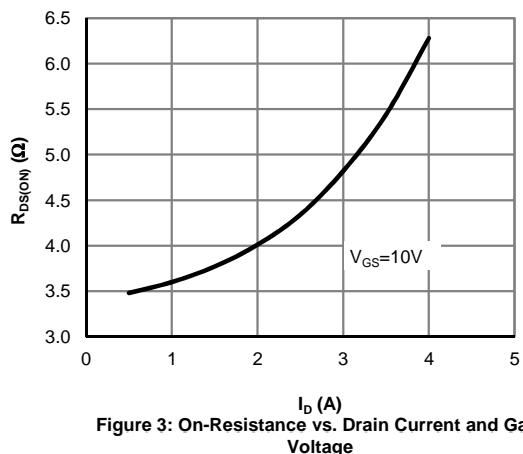
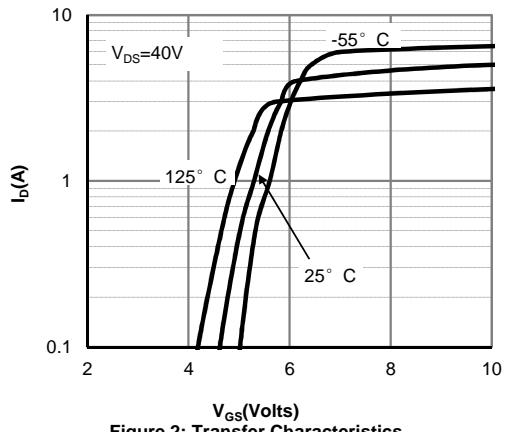
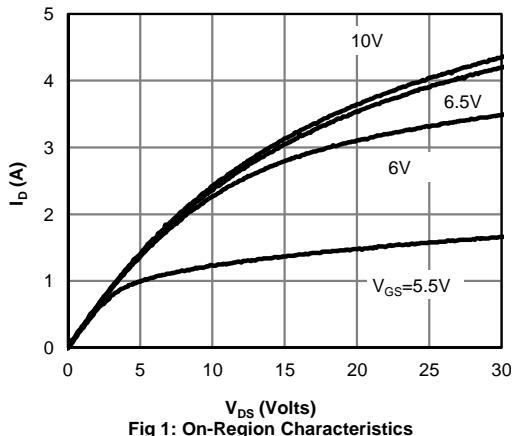
Thermal Characteristics				
Parameter	Symbol	Typical	Maximum	Units
Maximum Junction-to-Ambient ^{A,G}	$R_{\theta JA}$	45	55	$^\circ C/W$
Maximum Case-to-sink ^A	$R_{\theta CS}$	-	0.5	$^\circ C/W$
Maximum Junction-to-Case ^{D,F}	$R_{\theta JC}$	1.8	2.2	$^\circ C/W$

Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
STATIC PARAMETERS						
BV_{DSS}	Drain-Source Breakdown Voltage	$I_D=250\mu\text{A}, V_{GS}=0\text{V}, T_J=25^\circ\text{C}$	600			V
$BV_{DSS}/\Delta T_J$	Zero Gate Voltage Drain Current	$I_D=250\mu\text{A}, V_{GS}=0\text{V}$		0.56		$\text{V}/^\circ\text{C}$
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=600\text{V}, V_{GS}=0\text{V}$		1		μA
		$V_{DS}=480\text{V}, T_J=125^\circ\text{C}$		10		
I_{GSS}	Gate-Body leakage current	$V_{DS}=0\text{V}, V_{GS}=\pm 30\text{V}$			± 100	nA
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=5\text{V} I_D=250\mu\text{A}$	3	4	4.5	V
$R_{DS(\text{ON})}$	Static Drain-Source On-Resistance	$V_{GS}=10\text{V}, I_D=1\text{A}$		3.6	4.4	Ω
g_{FS}	Forward Transconductance	$V_{DS}=40\text{V}, I_D=1\text{A}$		3.5		S
V_{SD}	Diode Forward Voltage	$I_S=1\text{A}, V_{GS}=0\text{V}$		0.79	1	V
I_S	Maximum Body-Diode Continuous Current				2	A
I_{SM}	Maximum Body-Diode Pulsed Current				8	A
DYNAMIC PARAMETERS						
C_{iss}	Input Capacitance	$V_{GS}=0\text{V}, V_{DS}=25\text{V}, f=1\text{MHz}$	215	270	325	pF
C_{oss}	Output Capacitance		23	29	35	pF
C_{rss}	Reverse Transfer Capacitance		2.2	2.8	3.4	pF
R_g	Gate resistance	$V_{GS}=0\text{V}, V_{DS}=0\text{V}, f=1\text{MHz}$	3.5	4.4	6.6	Ω
SWITCHING PARAMETERS						
Q_g	Total Gate Charge	$V_{GS}=10\text{V}, V_{DS}=480\text{V}, I_D=2\text{A}$		9.5	11	nC
Q_{gs}	Gate Source Charge			1.9	2	nC
Q_{gd}	Gate Drain Charge			4.7	6	nC
$t_{D(on)}$	Turn-On Delay Time	$V_{GS}=10\text{V}, V_{DS}=300\text{V}, I_D=2\text{A}, R_G=25\Omega$		17.2	21	ns
t_r	Turn-On Rise Time			14.3	17	ns
$t_{D(off)}$	Turn-Off Delay Time			27	32	ns
t_f	Turn-Off Fall Time			17	20	ns
t_{rr}	Body Diode Reverse Recovery Time	$I_F=2\text{A}, dI/dt=100\text{A}/\mu\text{s}, V_{DS}=100\text{V}$		154	185	ns
Q_{rr}	Body Diode Reverse Recovery Charge	$I_F=2\text{A}, dI/dt=100\text{A}/\mu\text{s}, V_{DS}=100\text{V}$		0.8	0.96	μC

- A. The value of R_{QJA} is measured with the device in a still air environment with $T_A=25^\circ\text{C}$.
- B. The power dissipation P_D is based on $T_{J(MAX)}=150^\circ\text{C}$ in a TO252 package, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.
- C. Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)}=150^\circ\text{C}$.
- D. The R_{QJA} is the sum of the thermal impedance from junction to case R_{QJC} and case to ambient.
- E. The static characteristics in Figures 1 to 6 are obtained using $<300\ \mu\text{s}$ pulses, duty cycle 0.5% max.
- F. These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heatsink, assuming a maximum junction temperature of $T_{J(MAX)}=150^\circ\text{C}$.
- G. These tests are performed with the device mounted on 1 in² FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^\circ\text{C}$.
- H. L=60mH, $I_{AS}=2\text{A}$, $V_{DD}=150\text{V}$, $R_G=10\Omega$, Starting $T_J=25^\circ\text{C}$

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS



TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

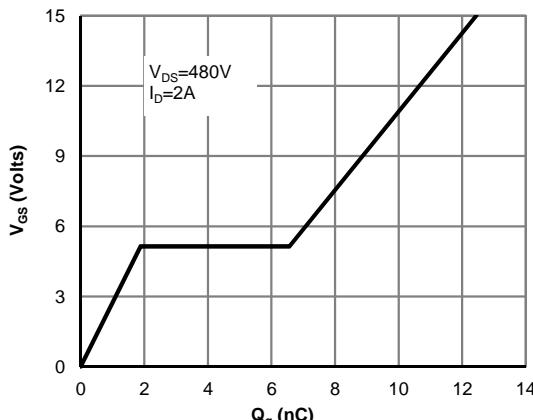


Figure 7: Gate-Charge Characteristics

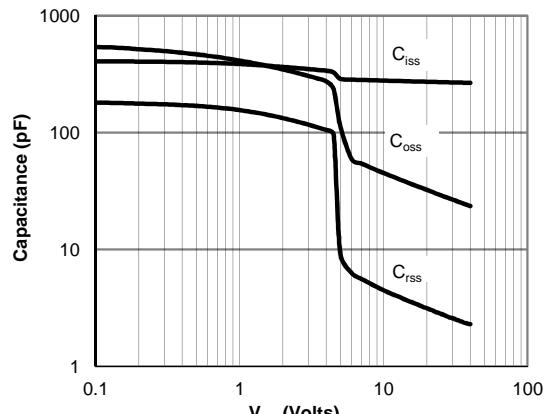


Figure 8: Capacitance Characteristics

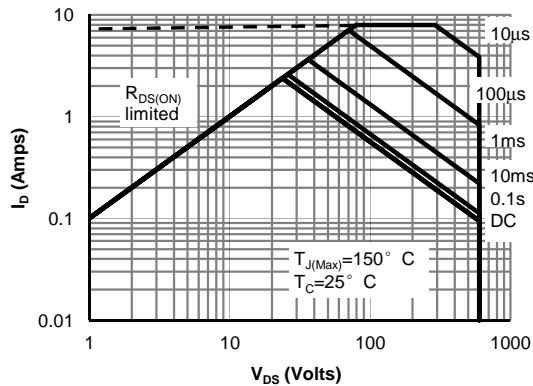


Figure 9: Maximum Forward Biased Safe Operating Area (Note F)

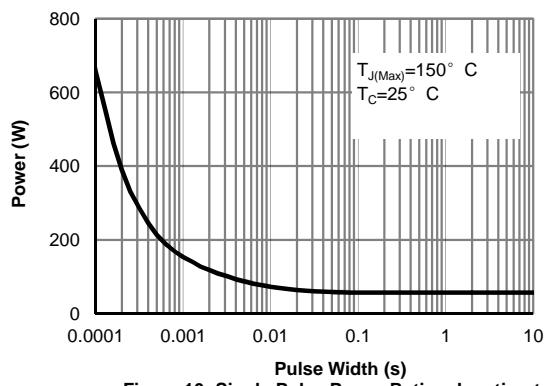


Figure 10: Single Pulse Power Rating Junction-to-Case (Note F)

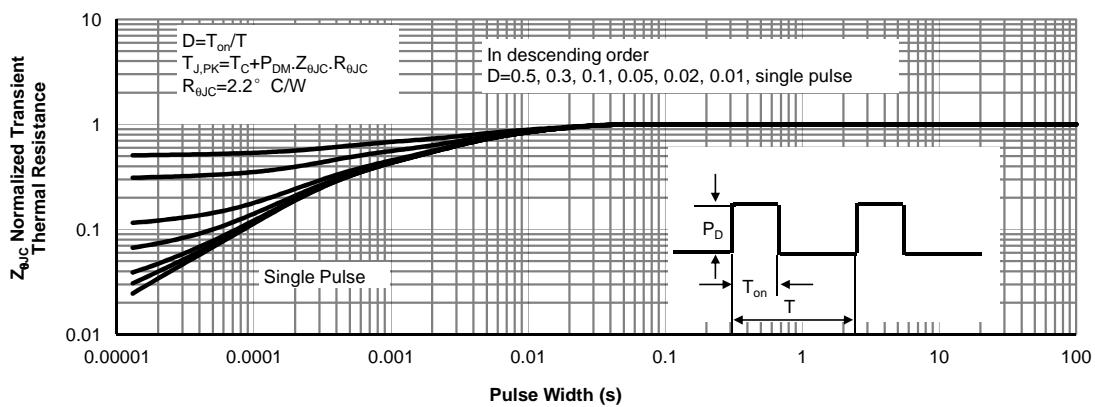


Figure 11: Normalized Maximum Transient Thermal Impedance (Note F)

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

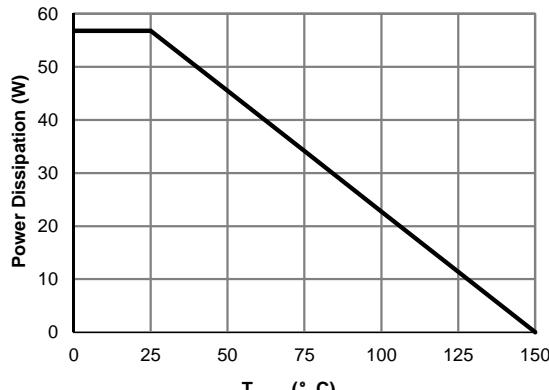


Figure 12: Power De-rating (Note B)

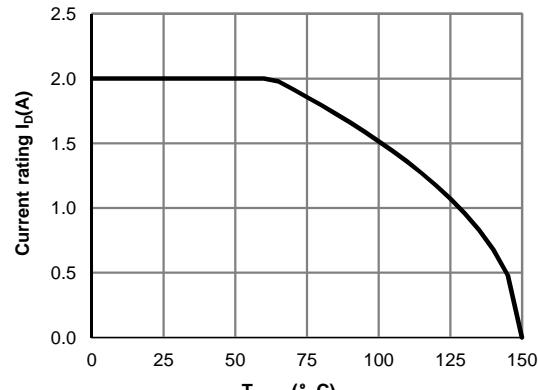


Figure 13: Current De-rating (Note B)

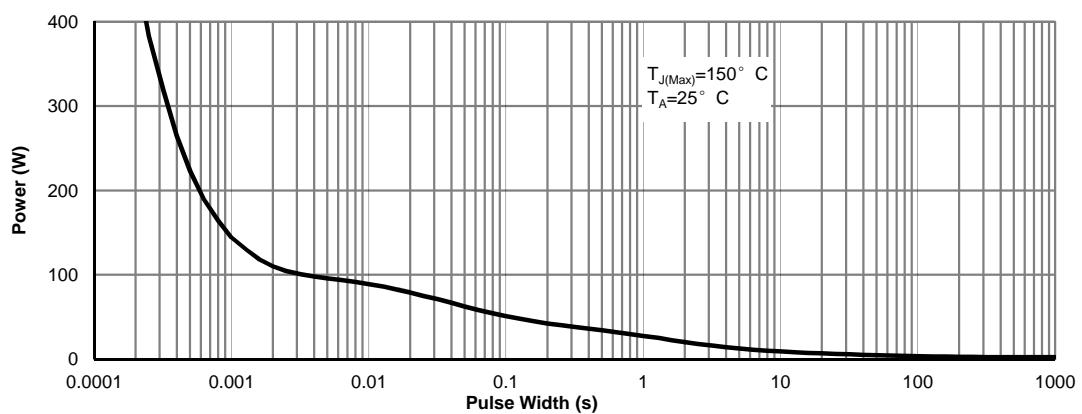


Figure 14: Single Pulse Power Rating Junction-to-Ambient (Note G)

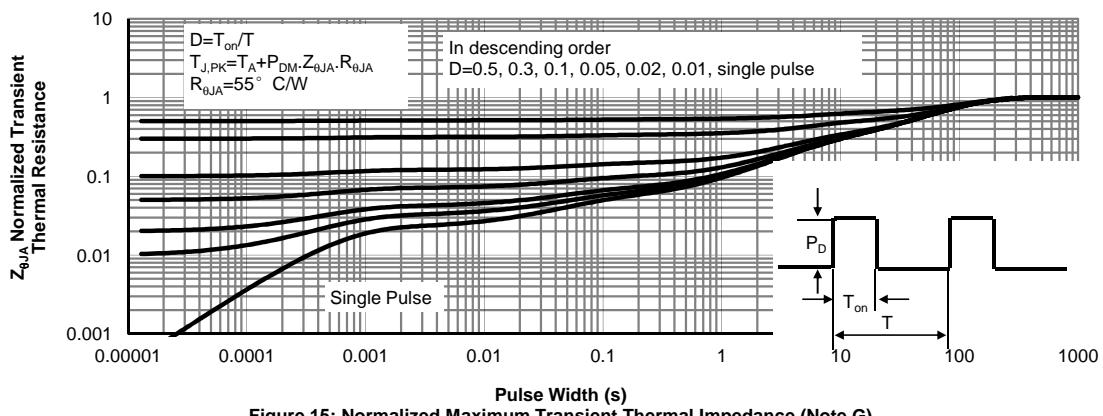
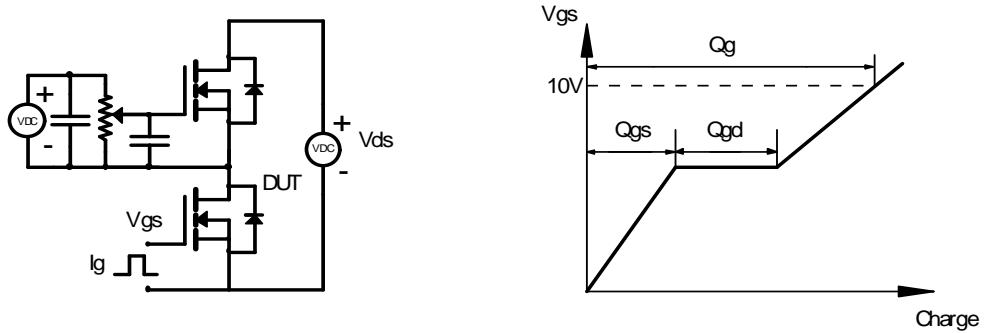
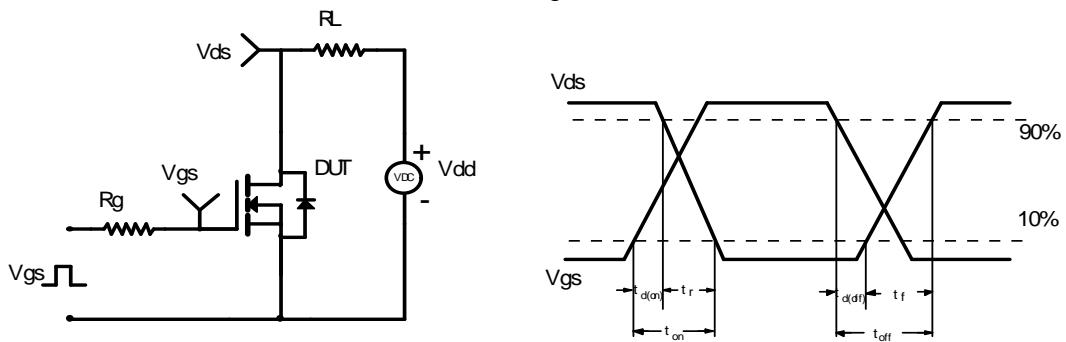


Figure 15: Normalized Maximum Transient Thermal Impedance (Note G)

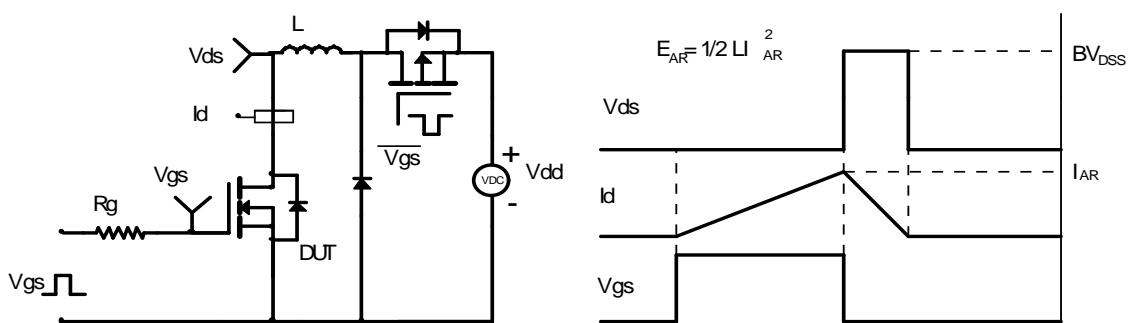
Gate Charge Test Circuit & Waveform



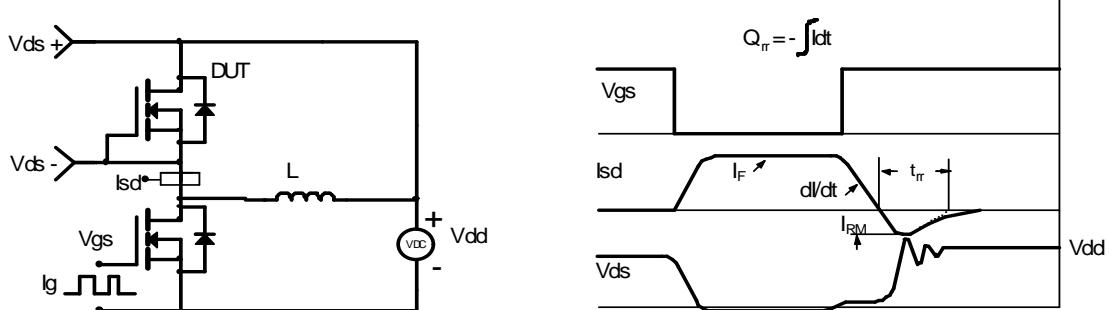
Resistive Switching Test Circuit & Waveforms



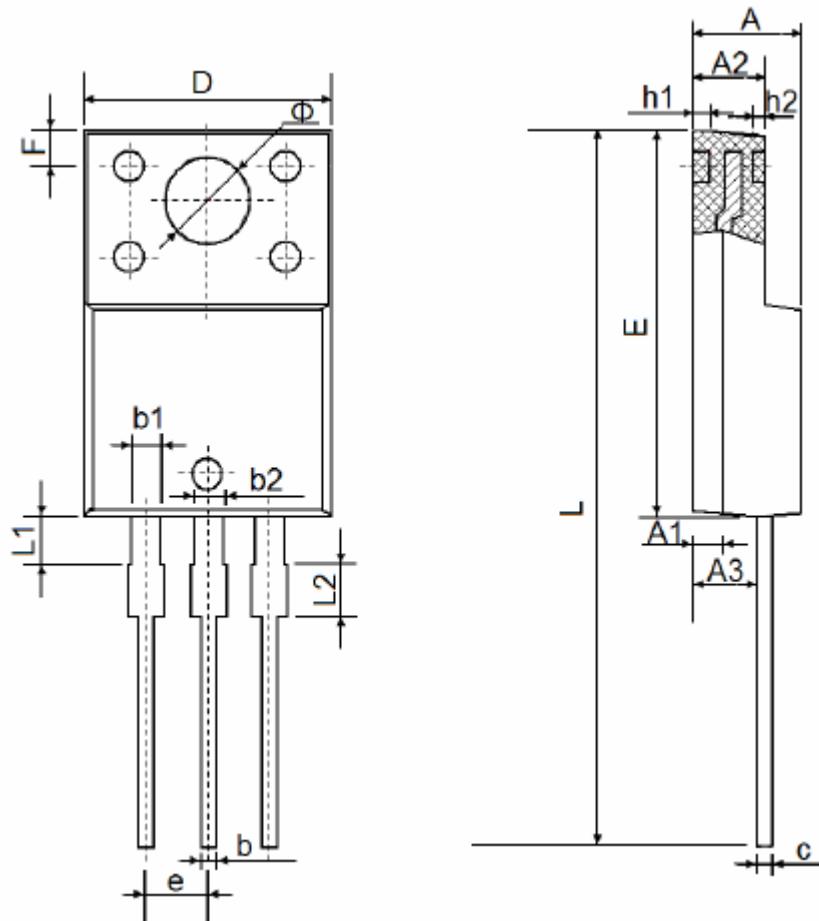
Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms



TO-220F Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	4.300	4.700	0.169	0.185
A1	1.300REF		0.051REF	
A2	2.800	3.200	0.110	0.126
A3	2.500	2.900	0.098	0.114
b	0.500	0.750	0.020	0.030
b1	1.100	1.350	0.043	0.053
b2	1.500	1.750	0.059	0.069
c	0.500	0.750	0.020	0.030
D	9.960	10.360	0.392	0.408
E	14.800	15.200	0.583	0.598
e	2.540TYP.		0.100TYP	
F	2.700REF		0.106REF	
Φ	3.500REF		0.138REF	
h1	0.800REF		0.031REF	
h2	0.500REF		0.020REF	
L	28.000	28.400	1.102	1.118
L1	1.700	1.900	0.067	0.075
L2	1.900	2.100	0.075	0.083